

**Amendments to the Abstract:**

Please delete the Abstract of the Disclosure on page 13, lines 14-23, and enter the following Abstract of the Disclosure on a separate page:

-- ABSTRACT OF THE DISCLOSURE

A method and system for automatically producing clock signals for sampling data signals at different data rates via a phase locked loop, wherein in a synchronization process by means of the phase locked loop, a data signal is sampled successively using a clock signal at different frequencies, which are associated with different transmission protocols, and is checked for the presence of protocol identification information associated with the selected clock signal, until protocol identification information is detected such that the frequency resolution of the phase locked loop is advantageously increased, and the synchronization of the clock signal to the data signal is improved. --